

Figure 1

FIG. 1 is a block diagram of a system architecture.

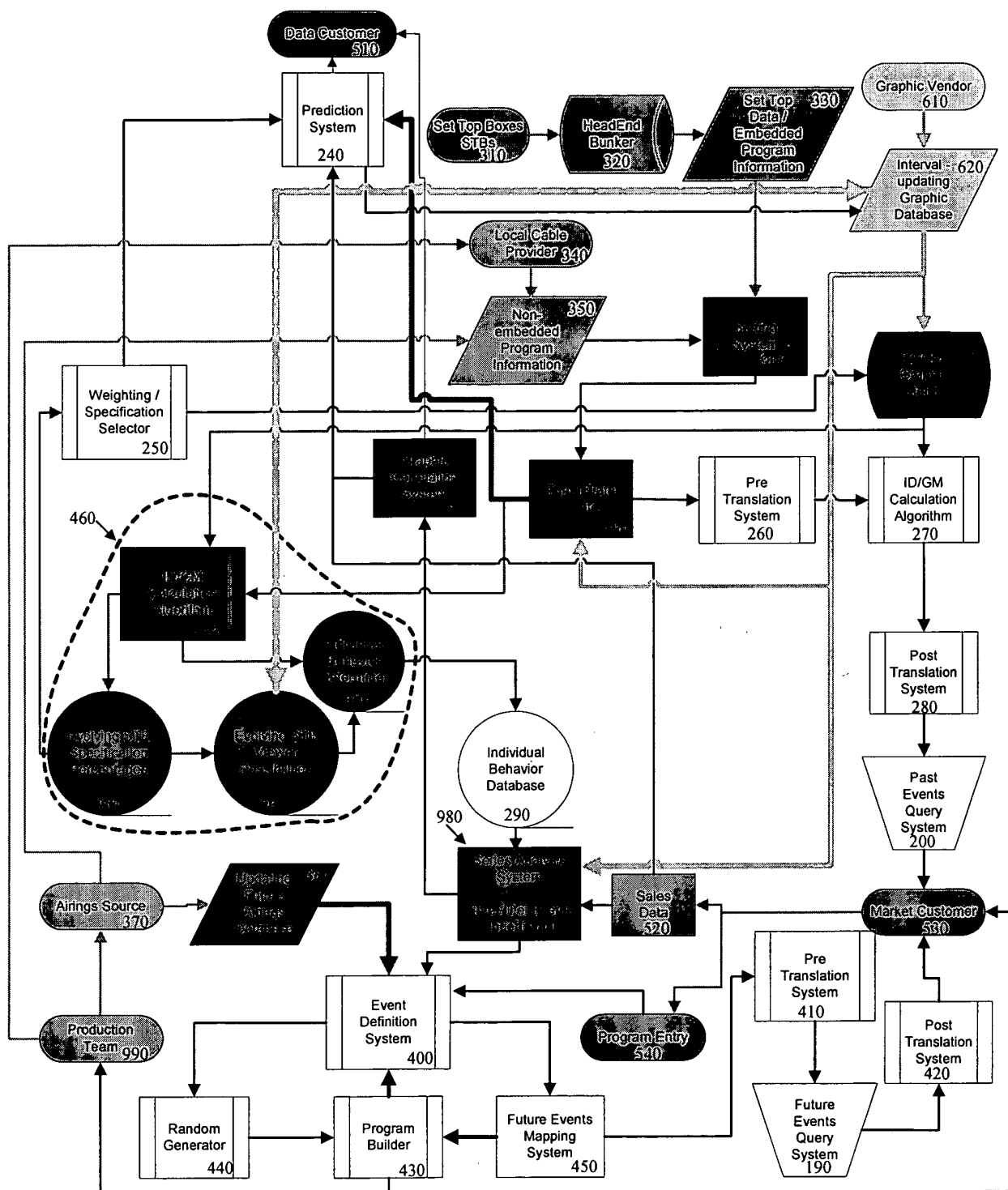


Figure 2

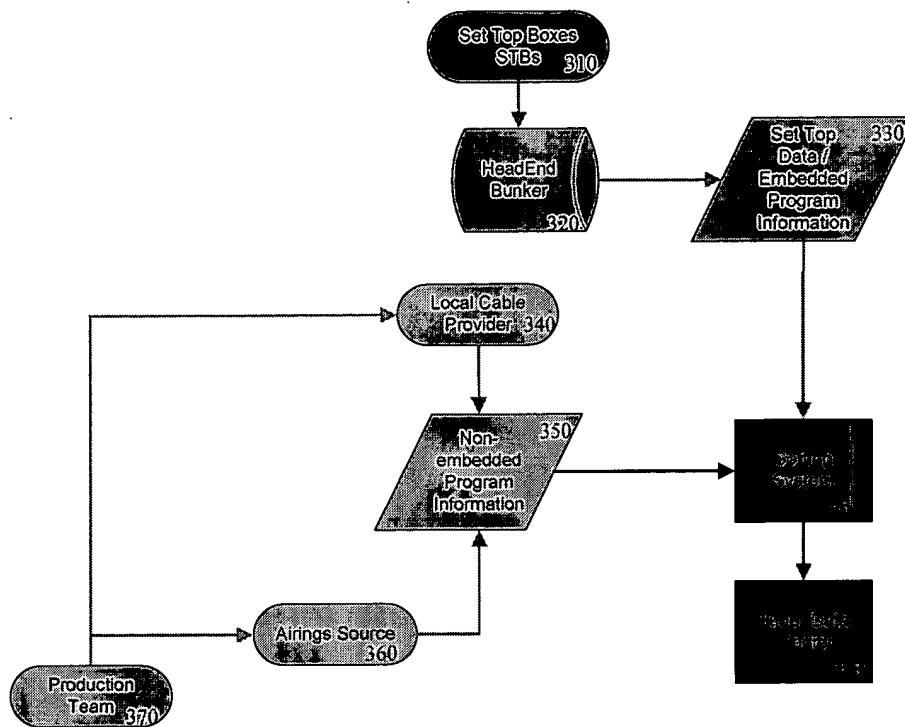


Figure 3

FOOTNOTES

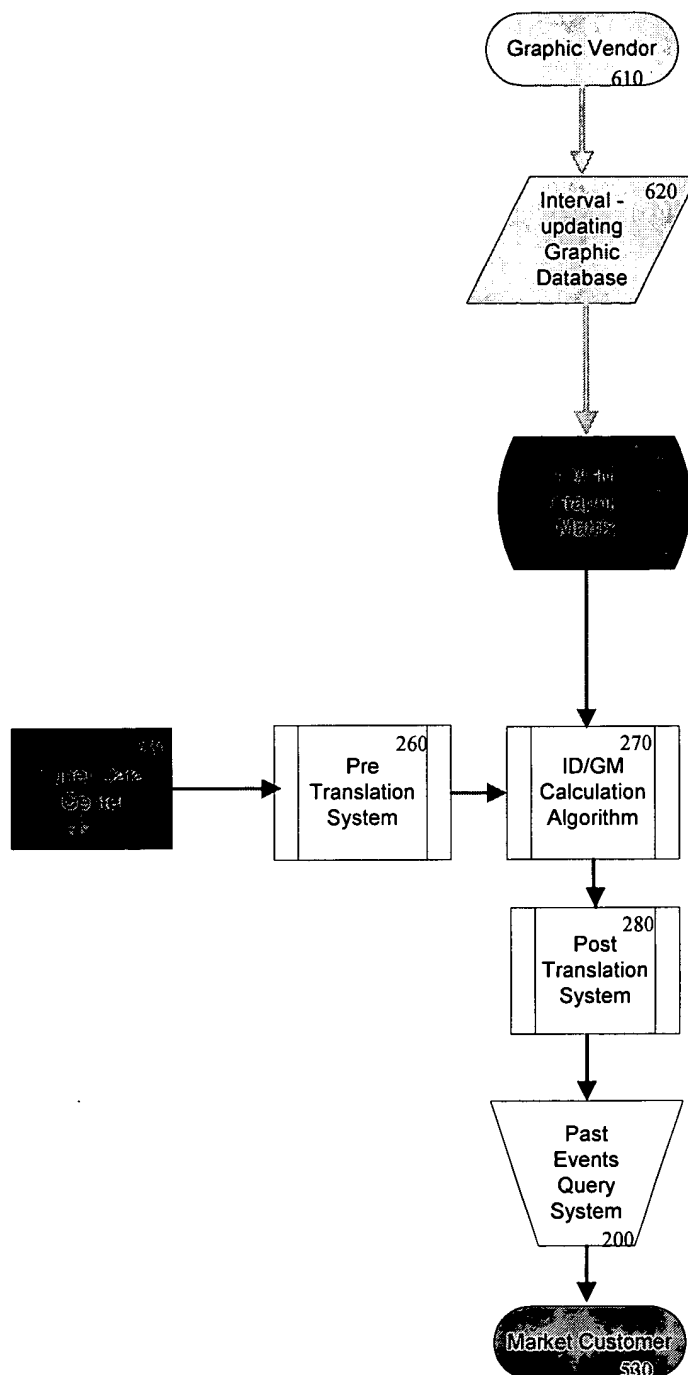


Figure 4

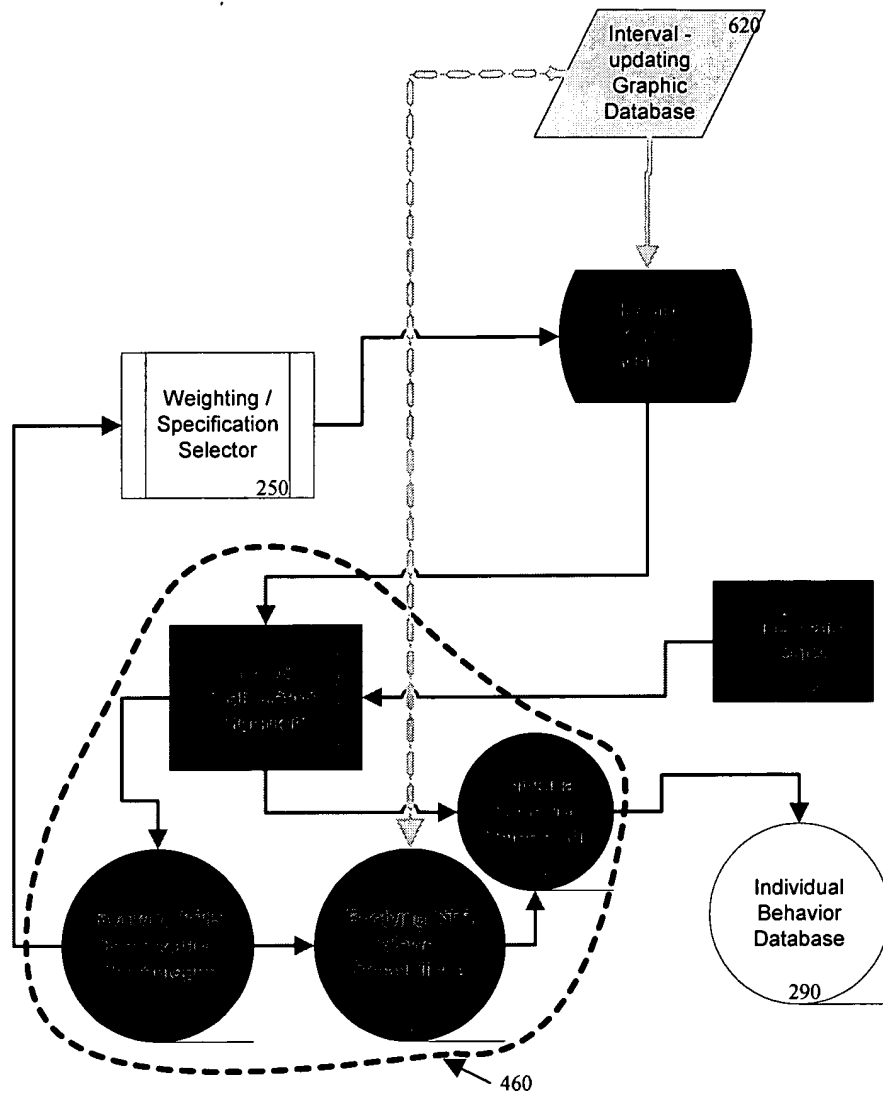


Figure 6

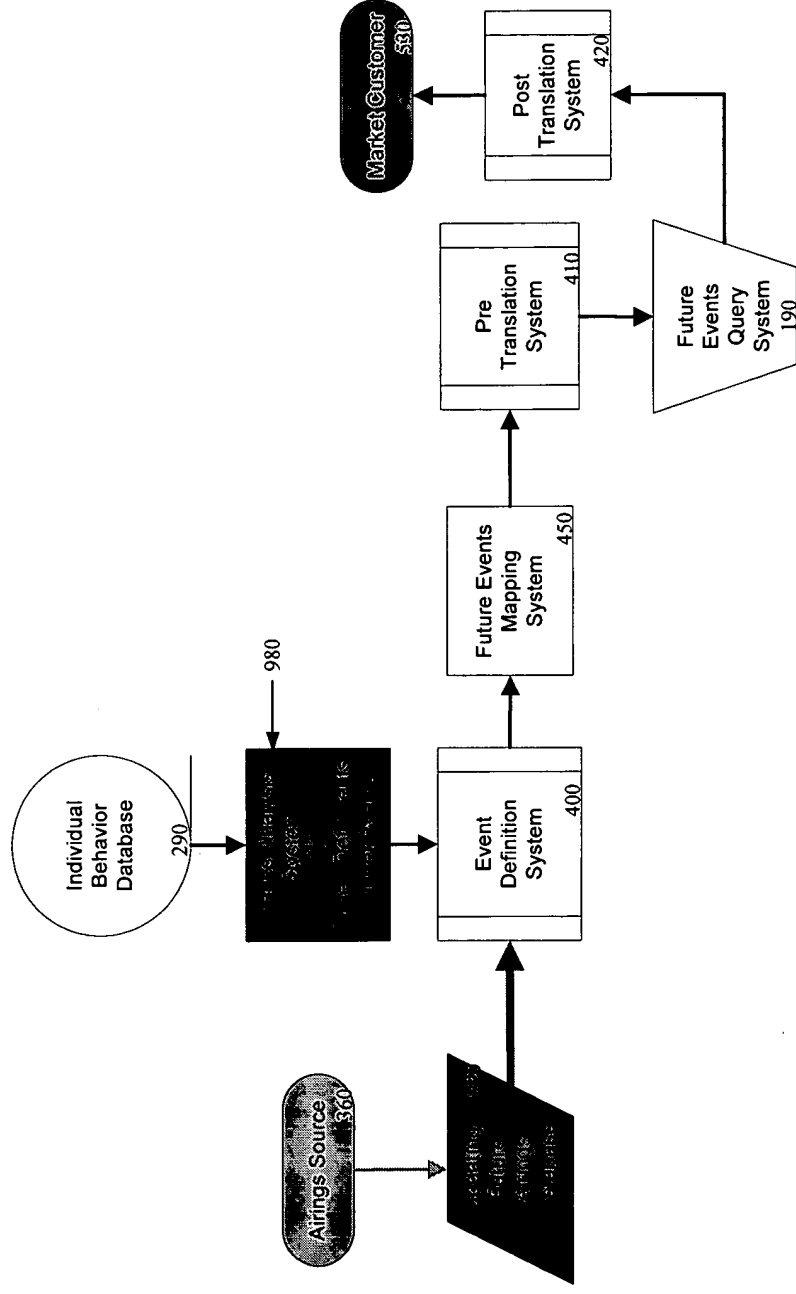


Figure 7

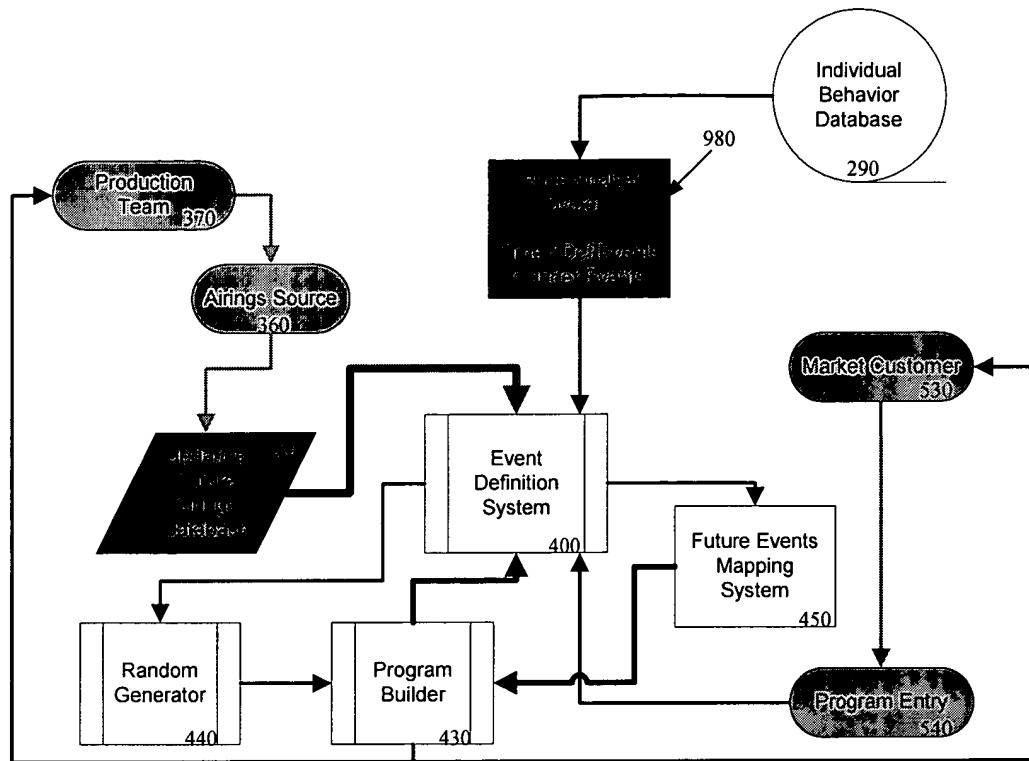


Figure 8

FIG. 8

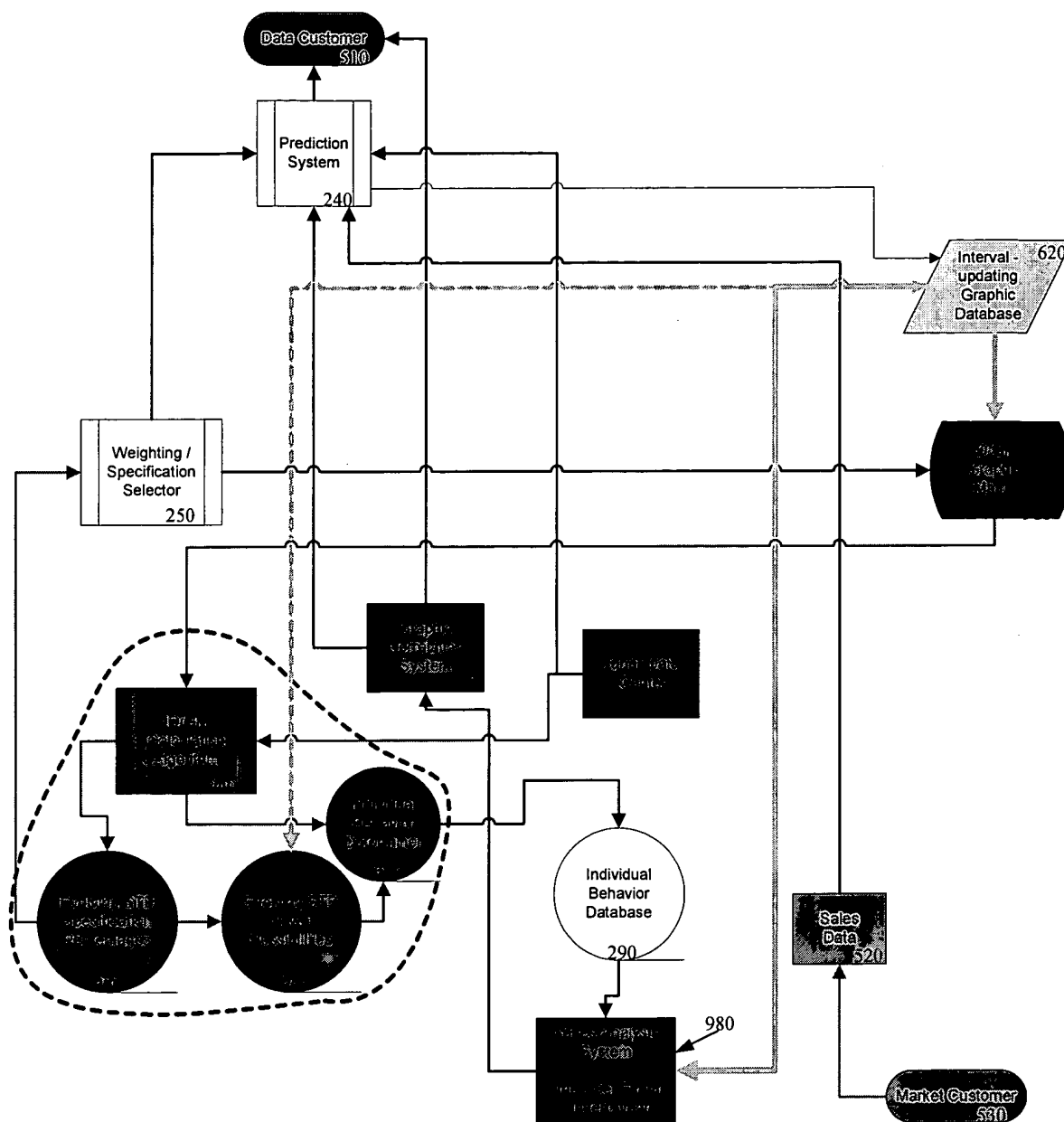


Figure 9

[illegible]

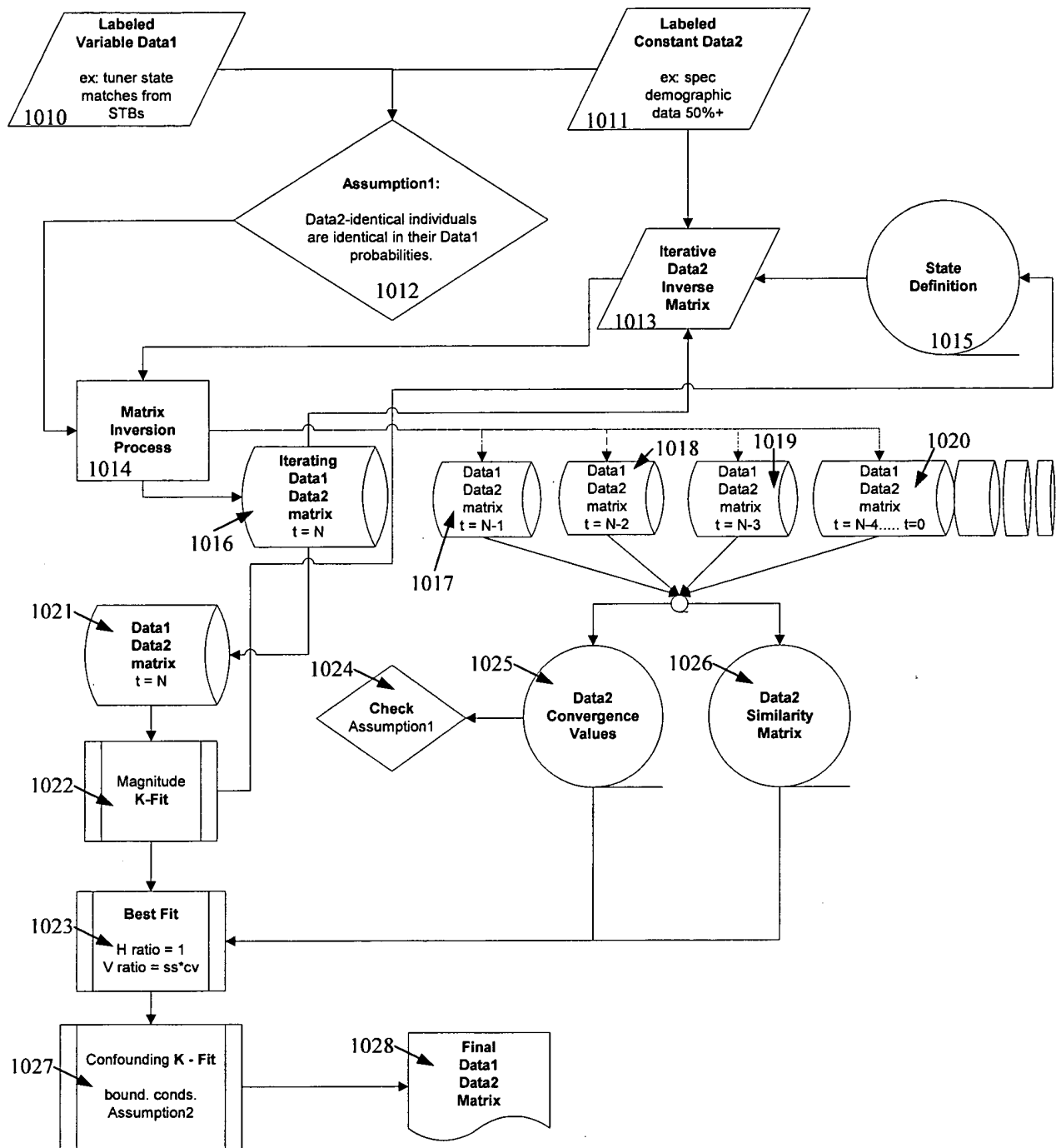


Figure 10

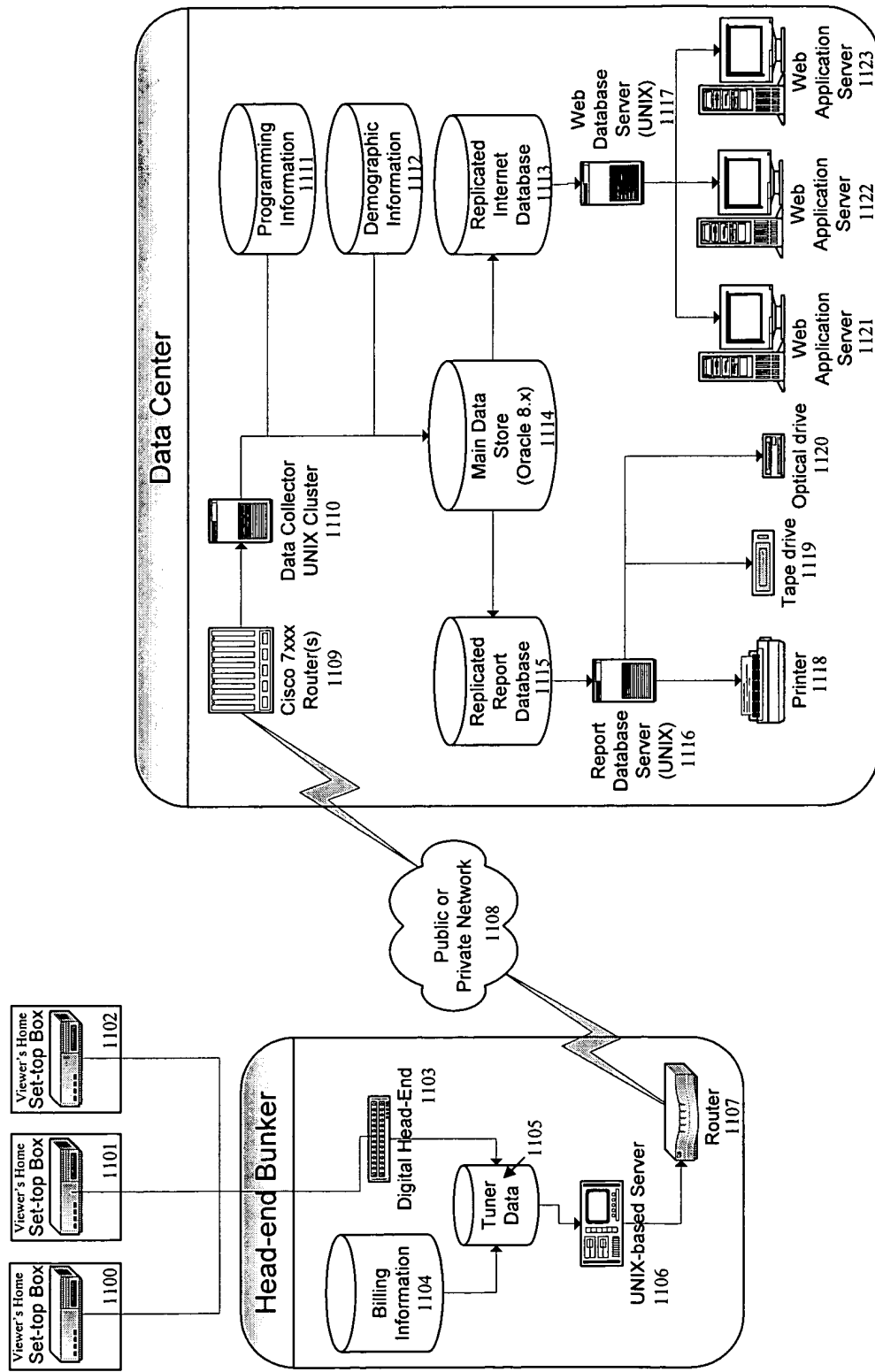


Figure 11

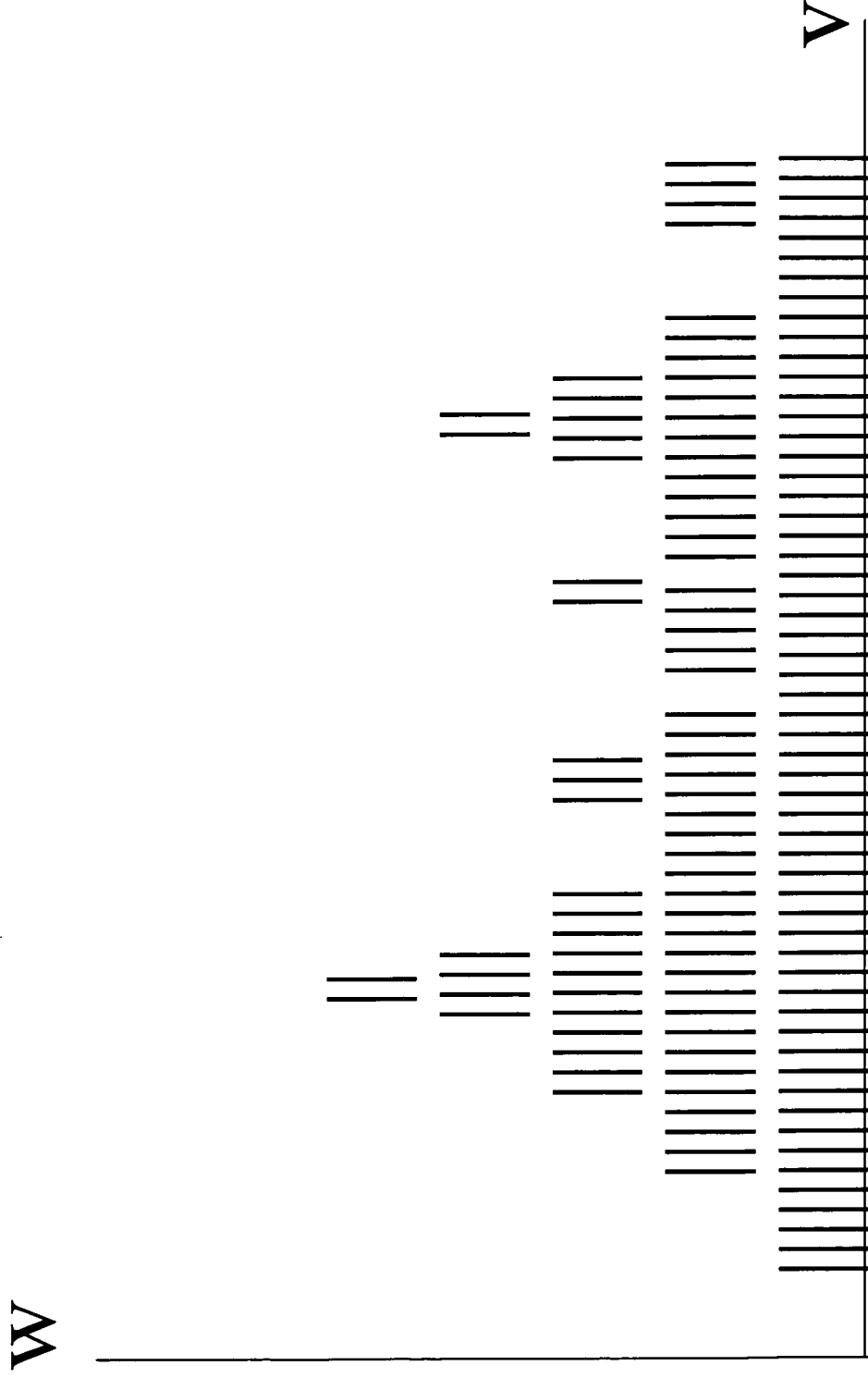


Figure 12

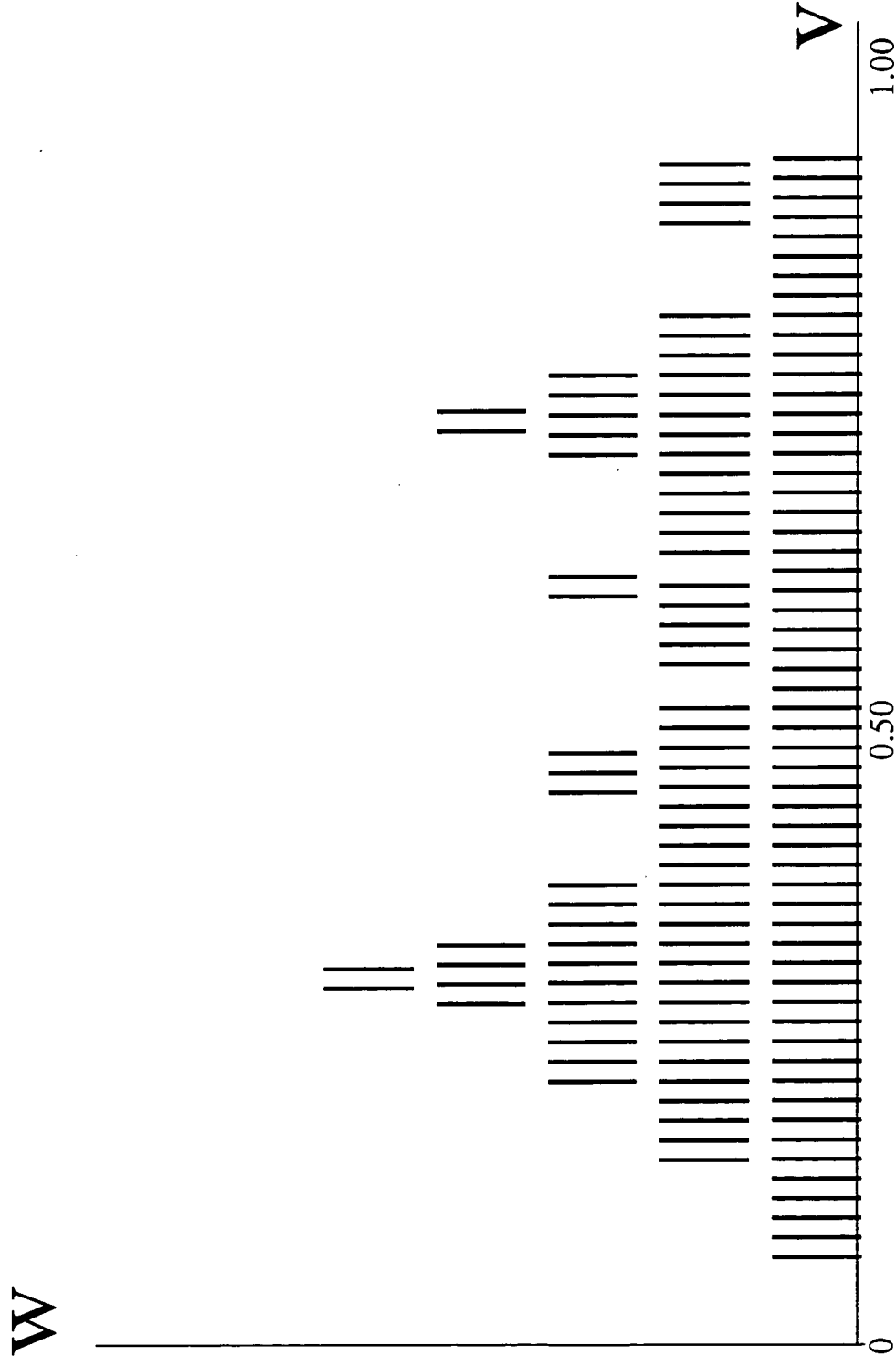


Figure 13

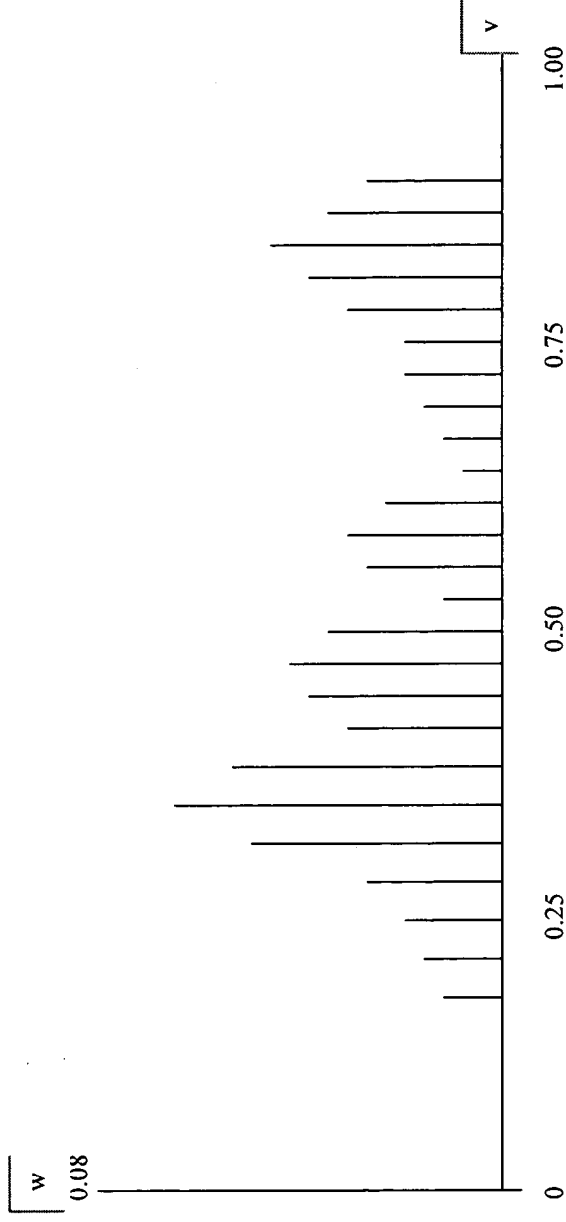


Figure 14

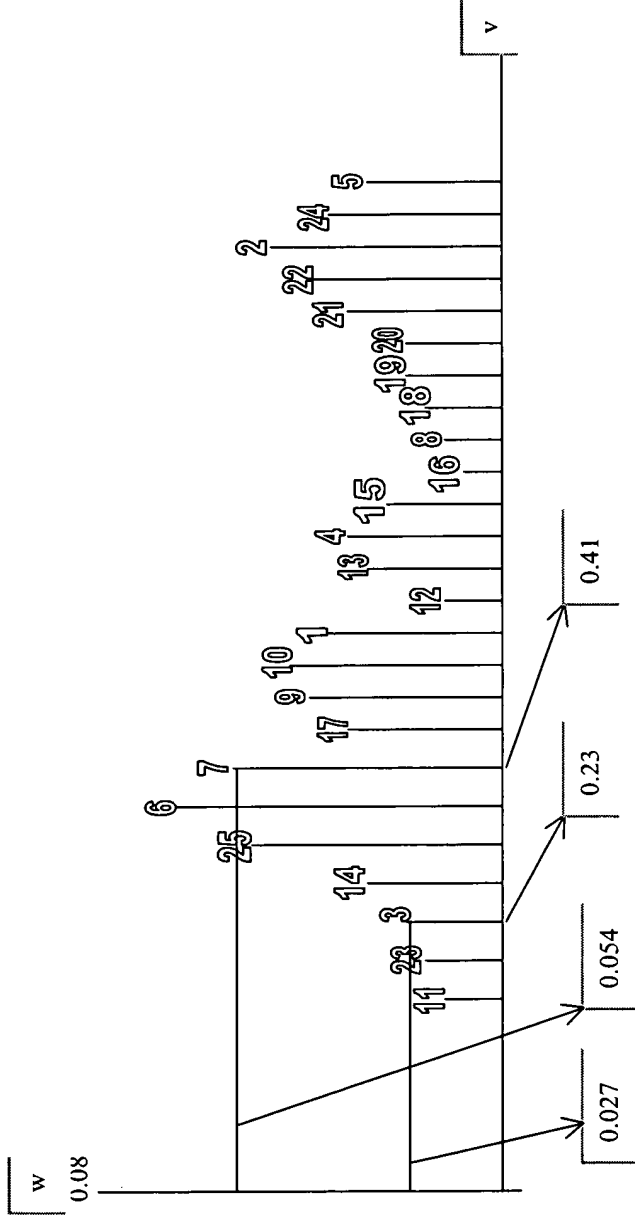


Figure 15



Figure 16



Figure 17

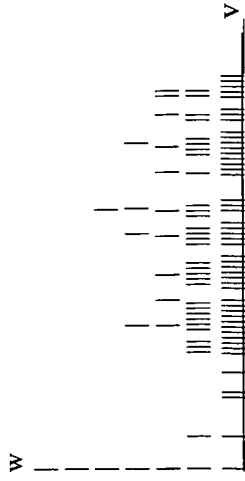


Figure 18